



May 2017 Newsletter

Intel® PCC Opportunities

We would like to encourage you to participate in the Intel Collaboration Hub as a speaker at ISC'17 in Frankfurt, Germany from June 18-24, 2017. Each slot is 20 minutes, and you can submit through [EasyChair](#). The deadline is May 5, 2017.

The Hartree Center will be hosting an [Intel® PCC Parallel Software Workshop](#) on July 13, 2017 in Warrington, UK. If you would like to submit a poster or speaking proposal for consideration, please email: luke.mason@stfc.ac.uk.

IXPUG Events

Embree, OSPRay, and OpenSWR: learn how the experts are using Intel® Xeon Phi™ processors to maximize their code for Software Defined Visualization by attending the IXPUG workshop on May 22-25, 2017 at the Texas Advanced Computing Center. Register [HERE](#).

The IXPUG Annual Spring Conference hosted by University of Cambridge was a success! If you weren't able to attend, you can view the presentations [HERE](#).

Intel® Xeon Phi™ Processor Access

We encourage you to optimize your application for multi-node by testing on the following clusters:

Texas Advanced Computing Center (TACC) Stampede Cluster:

- Click [HERE](#) and create a new account (**do not click on PI-eligible**) and follow the email instructions.
- Register account by emailing ipcc.program.office@intel.com with username.

Intel® Application Development Cluster (Endeavor):

- Request account by completing the form located [HERE](#).

Discount on Intel® Xeon Phi Processors Platforms: Order today and use code “**EDUCATION**” in the order form for a 10% discount. The systems are fully configured platforms and include CentOS, a 1 year license for Intel® Parallel Studio XE 2017 edition, free parallel programming training, and support from Colfax!

Training Opportunities

Learn from and connect with other HPC developers by participating in the training opportunities below:

Date	Location	Event
May 3, 2017	Virtual	Healthy, Happy Performing Clusters
May 3-11, 2017	Virtual	Improving Performance on the Intel Xeon Phi Processor

May 8, 2017	Stockholm, Sweden	Intel Code Modernization Workshop
May 10, 2017	Virtual	Snapshot Your Performance and Improve!
May 10, 2017	Copenhagen, Denmark	Intel Code Modernization Workshop
May 11, 2017	Virtual	MC2 Series 003: Plasma Physics
May 15, 2017	Virtual	HOW Series: Deep Dive
May 16, 2017	Toulouse, France	Intel Code Modernization Workshop
May 17, 2017	Seoul, South Korea	Intel HPC Workshop - Lustre
May 17, 2017	Virtual	HPC Applications Deserve High Performance Analytics
May 18, 2017	Hunan, PRC	Intel Code Modernization Workshop
May 22, 2017	Austin, TX	IXPUG Software-Defined Visualization Workshop
May 23, 2017	Rome, Italy	Intel Code Modernization Workshop
May 24, 2017	Virtual	Boosting Application Performance with Standard C++ Algorithm
May 29, 2017	Poznan, Poland	Intel Code Modernization Workshop
May 31, 2017	Virtual	Accelerate Application Performance
Jun 7, 2017	Virtual	CPUs, GPUs, FPGAs: Managing the Alphabet Soup
Jun 8, 2017	Hunan, PRC	Intel Code Modernization Workshop
Jun 8, 2017	Virtual	Get the Most out of Your Free Trial of Intel Xeon Phi Processors
Jun 12, 2017	Dublin, Ireland	Intel Code Modernization Workshop
Jun 8, 2017	Warrington, UK	IPCC Parallel Software Workshop
Jun 14, 2017	Virtual	Julia for Machine Learning and Deep Learning
Anytime	Virtual	Intel® PCC IT4Innovations National Supercomputing Center

*NEW: Case Studies and Intel® Parallel Studio Beta Program

University of Edinburgh: [Grid](#) is an Edinburgh developed data parallel QCD package. It is designed to change data layouts to automatically adjust to the vector length of a given architecture, and delivers good performance (10-30%) throughout by structuring the Cartesian Mesh Library.

Moscow State University: MU has developed special functions in quantum chemistry with Electrostatic interaction of charged Gaussian clouds. They did this by using electron repulsion integral optimizations over Cartesian Gaussians.

Intel® Parallel Studio XE 2018 Beta Program: Get early access to the latest updates to Parallel Studio XE. The deadline to apply is June 22nd, 2017. Learn more about new features and details for registration [HERE](#).

More News...

Keep up with the latest in HPC news:

- [Cineca's HPC System Tackles Italy's Biggest Computing Challenges](#)
- [Intel Releases Optimized Python for HPC](#)
- [CERN openlab Explores New CPU/FPGA Processing Solutions](#)
- [Driving Better Answers to Life's Tiny Questions](#)
- [Making Sense of When to Use FPGAs](#)

