November 2017 Newsletter

Intel® HPC Developer Conference & SC17

Come gain hands-on experience with Intel platforms, network with Intel and industry experts, and gain insights on recent technology advances to maximize software efficiency and accelerate your path to discovery.

Date	Event	Time(MST)	Location
November 11- 12, 2017	Intel® HPC Developer Conference	*See Agenda*	Sheraton Denver Downtown in Denver, Colorado
November 12, 2017	Intel® SC'17 Reception (If not registered for Intel® HPC Dev Conf)	7:00pm	Denver Museum of Nature and Science
November 13-16, 2017	Intel Booth: Community Hub Activities	*See Agenda*	Denver Convention Center, Intel Booth #1203
November 14, 2017	Intel® PCC Members Only Meeting (See separate email to RSVP)	6:00pm–9:30pm	Venice Ristorante (1700 Wynkoop St, Denver, CO 80202)
November 14, 2017	IXPUG BoF (agenda)	5:00pm–7:00pm	Denver Convention Center, Room 603
November 14, 2017	Omni-Path User Group (OPUG) Meeting BoF	12:15pm–1:15pm	Denver Convention Center, Room 702
November 15, 2017	OpenHPC BoF	12:15pm–1:15pm	Denver Convention Center, Room 507
November 15, 2017	SC17 Invited Talk by Judy Qiu on "Harp-DAAL: A Next Generation Platform for High Performance Machine Learning on HPC-Cloud"	10:30am–11:15am	Denver Convention Center, Mile High Ballroom
November 15, 2017	SC17 Invited Talk by Pradeep Dubey on "Artificial Intelligence and The Virtuous Cycle of Compute"	3:00pm–4:00pm	Denver Convention Center,, Mile High Ballroom

New Intel® PCC Center

Welcome our newest Intel® PCC member:

University of Stuttgart:
Visualization Research Center of the University of Stuttgart (VISUS) aim to modernize and restructure the MegaMol architecture to scale to current data set sizes and make MegaMol capable of running headless and in situ, either on the simulation nodes or a separate rendering cluster.

Free Training Opportunities：Join. Learn. Excel.

Join us at any of these upcoming educational workshops and conferences and learn about new Parallel Programming concepts, Intel® Libraries, Software Development tools and Artificial Intelligence frameworks. They are open to the public and free to attend.

Date	Location	Event
November 1, 2017	Virtual	Parallel Programming Standards Update: MPI*, OpenMP* and Intel® TBB
November 1, 2017	Oxford, UK	Intel® Code Mod Workshop by Bayncore
November 7, 2017	Madrid, Spain	Intel® Code Mod Workshop by Bayncore
November 8, 2017	Virtual	Better, Faster and More Scalable: The March To Exascale
November 11-12, 2017	Denver, CO	Intel® HPC Developer Conference 2017
November 14, 2017	Denver, CO	IXPUG BoF: Usability, Scalability & Productivity on Many-Core Processors - Intel Xeon Phi & Beyond
November 15, 2017	Zurich, Switzerland	Intel® Code Mod Workshop by Bayncore
November 16, 2017	Seoul, South Korea	Dae Han Code Mod Workshop
November 17, 2017	Seoul, South Korea	Dae Han Code Mod Workshop
November 23, 2017	Changchun, PRC	Paratera Code Mod Workshop
November 29, 2017	Shanghai, PRC	Intel-SJTU HPC Semiar
December 5, 2017	Germany	Software Developers Conference
December 8, 2017	Seoul, South Korea	Dae Han Code Mod Workshop
December 21, 2017	Harbin, PRC	Paratera Code Mod Workshop
January 28-31, 2018	Tokyo, Japan	IXPUG Workshop at HPC Asia

Access to Intel® Xeon Phi™ Processor

We encourage all Intel PCC members to leverage the TACC cluster to testing your optimized application for multi-node. To request access, please click **HERE** and create a new account (do not click on PI-eligible) and follow the email instructions. Please email the **ipcc.program.office@intel.com** account and include your username in the communication.

More News...

Check out these latest HPC news stories:

- **HPC China PAC2017 award**
- **INTEL-SJTU Symposium on HPC （ITOC）2017**
- **A New Class of Developer Workstations for Artificial Intelligence**
- **1000x Faster Deep-Learning at Petascale Using Intel Xeon Phi Processors**